

Form PTO-1449  
(Rev. 8-83)U.S. Department of Commerce  
Patent and Trademark Office

Attorney Docket No. 0756-1915

Based on Serial N 08/566,897  
09/235145

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant: Shunpei YAMAZAKI et al.

Filing Date: (not yet assigned)

Group: 2871

U.S. PTO  
101/15/99  
JC125  
2M  
01/15/99

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
TD	5,495,353	2/27/96	Yamazaki et al.	359	59	4/1994
TD	5,055,899	10/8/91	Wakai et al.	359	59	4/1990
TD	5,051,570	9/24/91	Tsujikawa et al.	359	59	1/1990
TD	5,012,228	4/30/91	Masuda et al.	350	333	
TD	4,818,077	4/4/89	Ohwada et al.	350	333x	
TD	4,680,580	7/14/87	Kawahara	350	784	
TD	4,949,141	8/14/90	Busta	357	23.7	
TD	4,103,297	7/25/78	McGreivy et al.	350	334x	
TD	5,250,931	10/5/93	Misawa et al.			

## FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
TD	53-144297	12/15/78	Japan	340	784	Abst	
TD	64-68724	3/14/89	Japan	340	784	Abst	
TD	0 321 073	6/21/89	European	340	784	X	
TD	2-51129	2/21/90	Japan	340	784	Abst	
TD	1-130131	5/23/89	Japan	340	784	Abst	
TD	64-68728	3/14/89	Japan			Abst	
TD	2-294622	12/05/90	Japan			Abst	
TD	2-278749	11/15/90	Japan			Abst	
TD	49-77537	07/26/74	Japan			Abst	
TD	62-126677	06/08/87	Japan			Abst	
TD	63-100777	05/02/88	Japan			Abst	
TD	64-30272	02/01/89	Japan			Abst	
TD	2-234134	9/1990	Japan	349	138	X	

Examiner T. DVONGDate Considered 6/18/99

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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**U.S. PATENT DOCUMENTS**

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TD	5,200,846	04/06/93	Hiroki et al.			
TD	5,327,001	07/05/94	Wakai et al.			
TD	5,612,799	03/18/97	Yamazaki et al.			

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
TD	01-165172	6/29/89	Japan			abst.
TD	02-085826	3/27/90	Japan			abst.
TD	62-014472	01/23/87	Japan			abst.
TD	02-277244	11/13/90	Japan			abst.
TD	03-034434	2/14/91	Japan			abst.
TD	01-076036	3/22/89	Japan			abst.
TD	02-210330	8/21/90	Japan			abst.
TD	02-188723	8/24/90	Japan			abst.
TD	01-30272	2/1/89	Japan			abst.
TD	63-100777	5/2/88	Japan			abst.
TD	62-126677	6/8/87	Japan			abst.
TD	49-77537	7/26/74	Japan			abst.
TD	55-32026	3/6/80	Japan			abst.
TD	61-141174	6/23/86	Japan			abst.
TD	2-188723	07/24/90	Japan			abst.
TD	2-210330	08/21/90	Japan			abst.
TD	1-156725	06/20/89	Japan			X
TD	55-32026	03/06/80	Japan			X
TD	61-141174	06/28/86	Japan			X

**OTHER DOCUMENTS (Including Author, Title, Relevant Pages, Date, Place of Publication)**


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TD	5,159,476	10/27/92	Hayashi			
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TD	4,868,137	09/19/89	Kubota			
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TD	5,097,311	03/17/92	Iwase et al.			

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	Document Number	Date	Country	Class	Subclass	Translation Yes      No
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TD	0298255	1/1989	EPO			Abst
TD	0096636	11/1988	Japan			Abst
TD	58-32466	02/25/83	Japan			Abst
TD	58-81972	07/04/81	Japan			Abst
TD	58-124273	07/23/83	Japan			Abst

## OTHER DOCUMENTS (Including Author, Title, Relevant Pages, Date, Place of Publication)

TD	Wolf et al., Silicon Processing for the VLSI ERA, Lattice Press, Volume 1, 1989, pages 151-154
TD	Wolf et al., Silicon Processing for the VLSI ERA, Lattice Press, Volume 2, pages 62-72.

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<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if approp.)
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TD		5,003,356	03/26/91	Wakai et al.	—	—	
TD		5,056,895	10/15/91	Kahn	—	—	
TD		4,239,346	12/16/80	Lloyd	—	—	
TD		5,251,071	10/93	Kusukawa et al.	—	—	
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
TD		58-115864	07/09/83	Japan	—	—	Abst
TD		59-214262	12/04/84	Japan	—	—	Abst
TD		62-124769	06/06/87	Japan	—	—	Abst
TD		0368260	05/06/90	EP	—	—	X
TD		58-158967	09/21/83	Japan	—	—	Abst
<b>OTHER DOCUMENTS (Including Author, Title, Relevant Pages, Date, Place of Publication)</b>							
TD		1990 IEEE International Solid-State Circuits Conference Digest of Technical Papers; 16 Feb. 1990, San Francisco, CA, pgs. 220-221, XP000201945, S.N. Lee et al., "A 5x9 Inch Polysilicon Gray-Scale Color Head Down Display Chip"					
TD		PROCESSING OF THE SID, Vol. 31, No. 4, 1990, Los Angeles, CA, pgs. 343-347, XP000207744, S. Takahashi et al., "A 10-in.-Diagonal 16-Gray-Level (4096 Color) a-Si TFT-LCD"					
Examiner	T. DUCN6		Date Considered		C/18/99		
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